

IN THE SPECIFICATION:

Please replace line 21 on page 3 with the following:

middleware applications 22 may be called by game program 20 through an associated

Please replace line 7 on page 15 with the following:

line. CPU 10 having internal memory cache(s) 15 and drivers 14 is conventionally connected to one

Please replace line 14 on page 15 with the following:

directed through the PPU driver by at least game program 20. With this

Please replace line 22 on page 20 with the following:

addresses). Two more AGU's (70C and 70[[E]]E) generate addresses for SPM 55 and

Please replace line 9 on page 21 with the following:

~~designed~~ design readily facilitates bi-directional data transfers between PMM 65 and IEM

Please replace line 11 on page 27 with the following:

Registers 162, an Execution Unit 163, a Load/Store Unit ~~462~~ 161, a Decode Unit 164, and

Please replace line 1 on page 44 with the following:

example, a software-based physics engine, and/or a 3-D effects engine, ~~here,~~ engine,
here shown